## Compute Express Link ™

# Compute Express Link<sup>TM</sup> (CXL<sup>TM</sup>)

**Engineering Change Notice to the Specification 2.0** 

September 2021 Compliance DOE return value

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CXL ENGINEERING CHANGE NOTICE

	TITLE:	Compliance DOE return value
	DATE:	Introduced date (03/03/2021)
		Updated date (04/09/2021)
	AFFECTED DOCUMENT:	CXL 2.0 Specification
	SPONSOR:	Nathan White (Intel)
		Ravi Karanam (Nvidia)

Part I

#### 1. Summary of Functional Changes

Compliance DOE provides a "request' to query the supported DOE capabilities. This returns a bitmask of which requests, by ID, are supported. Some of the DOE requests such a Test Algorithm setup have a complex set of available options. This extends the return value of the Compliance Mode DOE request 1 "Query Capabilities" to return additional information regarding test algorithm write semantics and establishes a pattern to provide similar information to future capabilities.

#### 2. Benefits as a Result of the Changes

Compliance software will be able to query the supported compliance capabilities of the device

#### <u>Assessment of the Impact</u> Device software will need to be extended to provide this value.

- 4. <u>Analysis of the Hardware Implications</u> No hardware changes expected
- <u>Analysis of the Software Implications</u> Software will need to extend the size of the "Query Capabilities" DOE return value to accommodate the additional information.
- 6. <u>Analysis of the Compliance and Test Implications</u> Compliance will be able to better determine the test algorithm capacities of the device.

. Part II

### Detailed Description of the change

In section 14.16.4.1 make the Add the following entry at the end of Table 278

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	1Ch 8	Compliance Capabilities options (See table xxx for Compliance Option value		
	ICH	Ū	descriptions)	
	<u>In sectio</u> the Com	n 14.16.4. pliance Cap	1 Add a table following Table 278. This table contains the bit field descripti pabilities options	ion of
0	Table : Descripti	xxx: Cor ion of the C	mpliance Options Value Descriptions Compliance option bit fields	
	Bits	Descriptio	on	
	15.0	Write Ser	mantics supported. Bitmask with the corresponding values	
		0 Set to 1	if Device supports CXL.cache and ItoMWr opcodes as requester	
		1 Set to 1	if Device supports CXL.cache and MemWr opcodes as requester	
		2 Set to 1	if Device supports CXL.cache and DirtyEvict opcodes as requester	
		3 Set to 1	if Device supports CXL.cache and WOWrInv opcodes as requester	
		4 Set to 1	if Device supports CXL.cache and WOWrInvF opcodes as requester	
	1010	5 Set to 1	if Device supports CXL.cache and WrInv opcodes as requester	
		6 Set to 1	if Device supports CXL.cache and CLFlush opcodes as requester	
		7 Set to 1	if Device supports CXL.cache and CleanEvict opcodes as requester	
$\mathbf{O}$		8 Set to 1 requester	. if Device supports CXL.cache and CleanEvictNoData opcodes as r	
		Bits 15:9	Reserved	
	31:16	Read Sem	nantics supported. Bitmask with the corresponding values	
		16 Set to	o 1 if Device supports CXL.cache and RdCurr opcodes as requester	
		17 Set to	1 if Device supports CXL.cache and RdOwn opcodes as requester	
		18 Set to	1 if Device supports CXL.cache and RdShared opcodes as requester	
		19 Set to	1 if Device supports CXL.cache and RdAny opcodes as requester	
		20 Set to	1 if Device supports CXL.cache and RdOwnNoData opcodes as requester	
		Bits 31:21	1 Reserved	
	47:32	32 Set to	1 if Device supports CXL.cache and CacheFlushed opcodes as requester	

		Bits 47:33 Reserved
	63:48	Reserved
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